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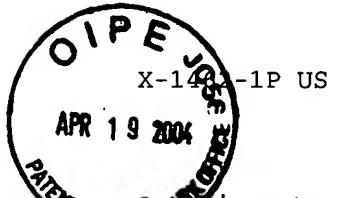
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PTO/SB/21 (01-03)
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	Application / Conf. N	10.	10/783,216		
TRANSMITTAL	Filing Date		February 20, 2004		
FORM	First Named Invent	or	Tim Tuan		
(to be used for all correspondence after initial	Examiner Name	· · ·	Unknown		
Mail Stop:		Art Unit	 :	Unknown	
Express Mail Receipt No.		Patent No.	*****		
Total Number of Pages in This Submission		Attorney Docket Nu	ımber	X-1462-1P US	
	ENCLOSUF	RES (check all that ap	ply)		
Amendment / Reply Preliminary Amendment After Final Affidavit(s)/declaration(s) Extension of Time Request Change Status to LARGE ENTITY Express Abandonment Request X Information Disclosure Statement (Supplemental) X Substitute PTO-1449(s) IDS by Applicant (PTO/SB/08A) Certified Copy of Priority Document(s)	nment Papers Recordation Cover Shee ration / Oath ng(s) sing-related Papers n - nvert a ional Application of Attorney, Revocation ge of Correspondence ss nal Disclaimer est for Refund		After Allowance Communication to Appeal Communication to Board of Appeals and Interferences Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) Status Letter X Return Receipt Postcard X Other Enclosure(s) (please identify below): One copy each of the five (5) references cited.		
Response to Missing Parts/ Incomplete Application Response to Missing Parts under 37 CFR 1.52 or 1.53	Remarks				
SIGNATUR	RE OF APPL	ICANT, ATTORNI	EY, OF	RAGENT	
Firm or Customer Number Attn: Justin Liu	1	24309 (Customer Number)		Reg. Number 51,959	
Signature	14/1				
Date (April 16, 2004	Charge any ad to our Deposit	ditional f Account	ees required/credit any overpayment Number: 24-0040		
CERTIFICATE OF MAILING					
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on this date: April 16, 2004					
Typed or Printed Name Julie Matthews					
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This collection of information is rrequired by 37 CFR 1.17 and 1.27. The information is required to botain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.



IN THE UNITED STATES PATENT OFFICE

cants:

Tim Tuan et al.

Assignee:

Xilinx, Inc.

Title:

"Tuning Programmable Logic Devices for Low-Power

Design Implementation"

Serial No.:

10/783,216 File Date:

February 20, 2004

Examiner:

Unknown Art Unit:

Unknown

Docket No.: X-1462-1P US

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, Virginia 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the five (5) references listed in the attached Substitute for Form PTO-1449A/PT. A copy of each is enclosed herein.

These references were cited in related U.S. patent applications. This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

Justin Liu

Attorney for Applicants

Reg. No. 51,959

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Julie Matthews

Name

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

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Complete if Known				
Application / Conf. No.	10/783,216 /			
Filing Date	February 20, 2004	<u> </u>		
First Named Inventor	Tim Tuan			
Art Unit	Unknown			
Examiner Name	Unknown			
Attorney Docket Number	X-1462-1P US			

U.S. PATENT DOCUMENTS						
Examiner Initials *	Cite No.¹	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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	FOREIGN PATENT DOCUMENTS						
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	Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY	Applicant of Cited Document	or Relevant Figures Appear			
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Examiner Signature	Date Considered	

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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁵ Applicant is to place a check mark here if English language Translation is attached.

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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449A/PTO		Complete if Known			
		Application / Conf. No.	10/783,216 /		
INFORMATION DISCLOSURE				Filing Date	February 20, 2004
STATEMENT BY APPLICANT			PI ICANT	First Named Inventor	Tim Tuan
				Art Unit	Unknown
(use as many sheets as necessary)		Examiner Name	Unknown		
Sheet	2	of	2	Attorney Docket Number	X-1462-1P US

OTHER NON PATENT LITERATURE DOCUMENTS					
Examiner Initials *	Cite No ^{.1}	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	· 2		
		MUTOH, S. et al., "1-V Power Supply High-Speed Digital Circuit Technology with Multithreshold-Voltage CMOS," August 1995, pages 847-854, Vol. 30, No. 8, available from IEEE Journal of Solid-State Circuits, 3 Park Avenue, 17th Floor, New York, NY 10016-5997.			
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		INUKAI, T. et al., "Boosted Gate MOS (BGMOS): Device/Circuit Cooperation Scheme to Achieve Leakage-Free Giga-Scale Integration," 2000, pages 409-412, available from IEEE Journal of Solid-State Circuits, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997.			
		HAMZAOGLU, F. et al., "Circuit-Level Techniques to Control Gate Leakage for sub-100nm CMOS," ISLPED, August 12-14, 2002, Pages 60-63, available from IEEE Journal of Solid-State Circuits, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997.			
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